**ECE** 272 Lab#2

Adders on an FPGA

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1. Introduction

The general purpose of this lab is to design one bit adder and then expand it to 4-bit adder by making complex combinational logic design. Furthermore

1. Design
2. Results
3. Experiment Notes
4. Study Question

Explain how you would convert your 4-bit adder to a 4-bit adder/subtractor.

Explain your what you did with the least-significant full-adder's carry-in. Why?